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#18

# REQUEST FOR ACCESS OF ABANDONED APPLICATION UNDER 37 CFR 1.14(a)

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| In re Application of | |
|---|---|
| HARARI Et. Al. | |
| **Application Number** | **Filed** |
| 337566 | 4/13/89 |
| **Group Art Unit** | **Examiner** |
| | |

Paper No. # 18

Assistant Commissioner for Patents
Washington, DC 20231

I hereby request access under 37 CFR 1.14(a)(3)(iv) to the application file record of the above-identified ABANDONED application, which is: (CHECK ONE)

X (A) referred to in United States Patent Number 5297148, column 1.

___ (B) referred to in an application that is open to public inspection as set forth in 37 CFR 1.11, i.e., Application No. _____, filed _____, on page _____ of paper number _____,

___ (C) an application that claims the benefit of the filing date of an application that is open to public inspection, i.e., Application No. _____, filed _____, or

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US005297148A

# United States Patent [19]

Harari et al.

[11] Patent Number: 5,297,148

[45] Date of Patent: Mar. 22, 1994

[54] FLASH EEPROM SYSTEM

[75] Inventors: Eliyahou Harari, Los Gatos; Robert D. Norman, San Jose; Sanjay Mehrotra, Milpitas, all of Calif.

[73] Assignee: SunDisk Corporation, Santa Clara, Calif.

[21] Appl. No.: 963,838

[22] Filed: Oct. 20, 1992

Related U.S. Application Data

[62] Division of Ser. No. 337,566, Apr. 13, 1989, abandoned.

[51] Int. Cl.[5] ..... G06F 11/00

[52] U.S. Cl. ..... 371/10.2; 371/10.1; 371/10.3; 365/200

[58] Field of Search ..... 371/10.2, 10.1, 10.3, 371/40.1; 365/200

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[57] ABSTRACT

A system of Flash EEprom memory chips with controlling circuits serves as non-volatile memory such as that provided by magnetic disk drives. Improvements include selective multiple sector erase, in which any combinations of Flash sectors may be erased together. Selective sectors among the selected combination may also be de-selected during the erase operation. Another improvement is the ability to remap and replace defective cells with substitute cells. The remapping is performed automatically as soon as a defective cell is detected. When the number of defects in a Flash sector becomes large, the whole sector is remapped. Yet another improvement is the use of a write cache to reduce the number of writes to the Flash EEprom memory, thereby minimizing the stress to the device from undergoing too many write/erase cycling.

4 Claims, 5 Drawing Sheets